



#### ERC grants for UGent professor Lieven Eeckhout

Professor [Lieven Eeckhout's](#) main research interests include computer architecture and the hardware/software interface with a specific emphasis on performance evaluation and modeling, and dynamic resource management. As professor at Ghent University, Faculty of Engineering and Architecture, Department of Electronics and Information Systems, he has obtained five prestigious European Research Council grants throughout his career: an ERC [Starting grant](#) in 2009 for the project '[DPMP](#) - Dependable Performance on Many-thread Processors' (approximately 1.3 M Euro), three ERC [Proof of Concept grants](#) in 2012 '[DaMon](#) - Datacenter Monitoring for Improving Insight and Efficiency', in 2015 '[Sniper-ARM](#) - High-Speed Architectural Simulation of ARM-based Systems' and in 2020 '[FSC](#) – Forward Slice Core' (together around €450k) and an ERC [Advanced grant](#) in 2016 for the project '[LSC](#) - Load Slice Core: A Power and Cost-Efficient Microarchitecture for the Future' (approximately 2.5 M Euro). Previously he has [shared his experiences](#) on applying for ERC grants.